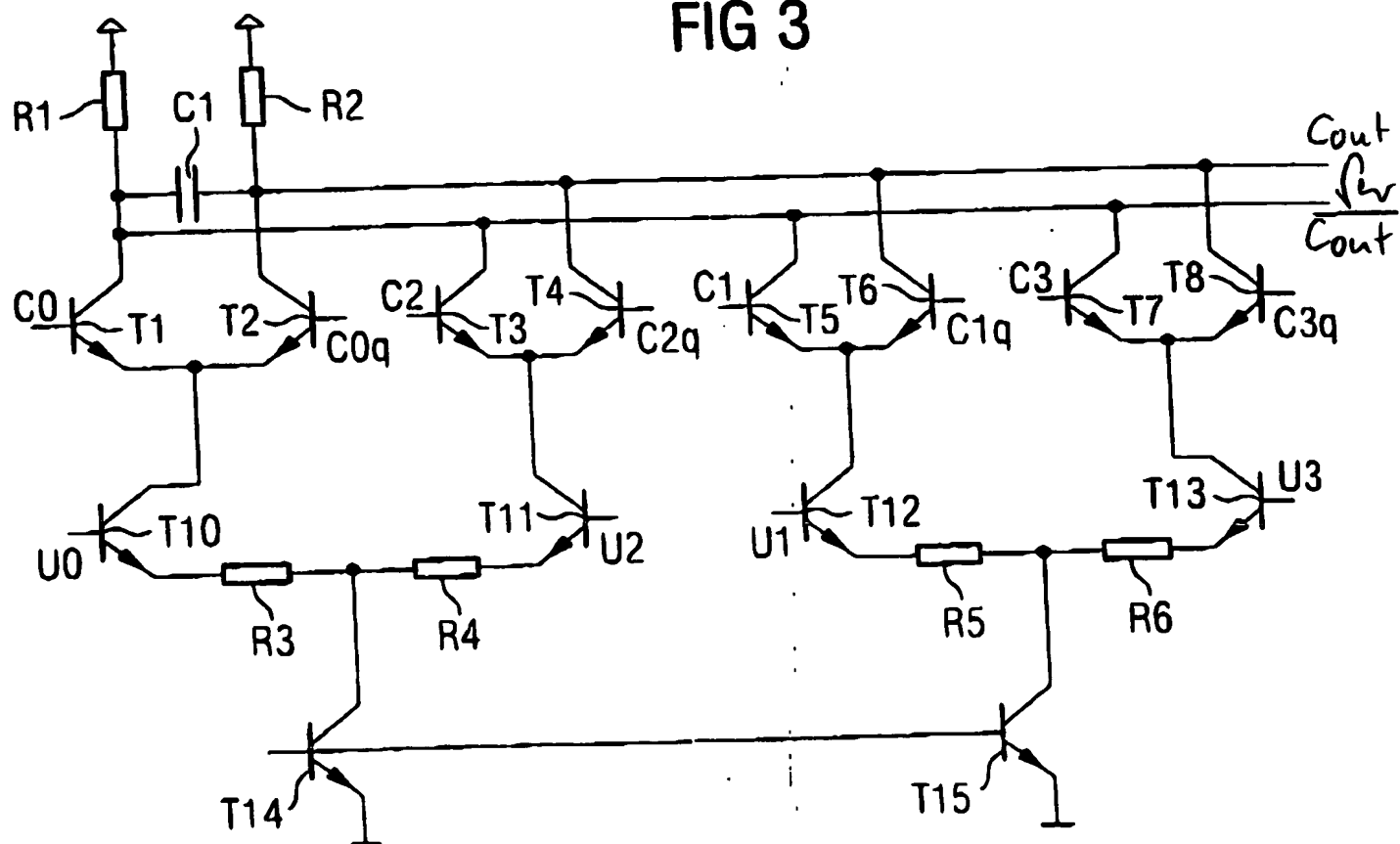


*) $\Delta\phi$ is a phase jump which occurs when the delay generator is changed over.

FIG 2



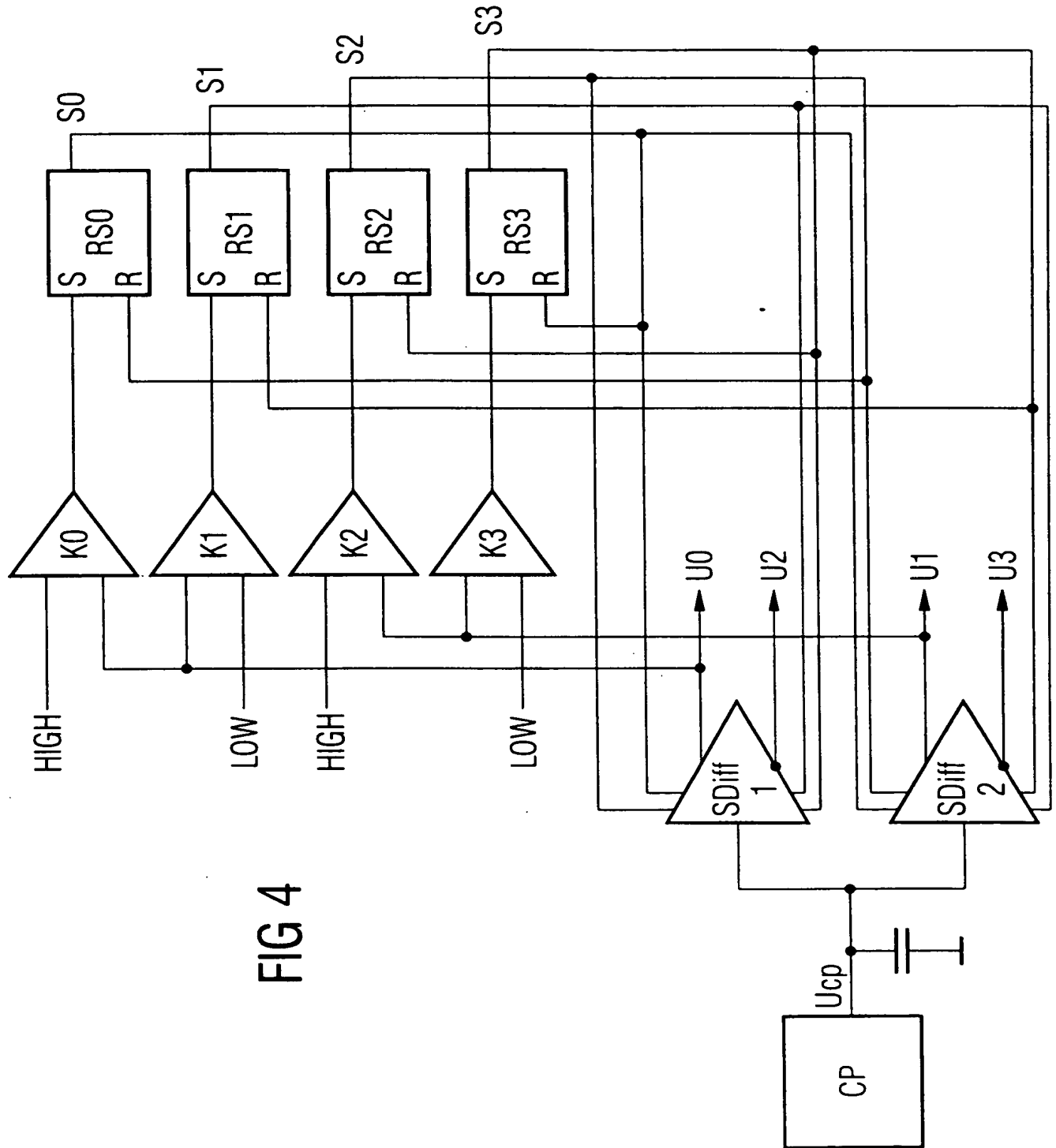


FIG 4

FIG 5

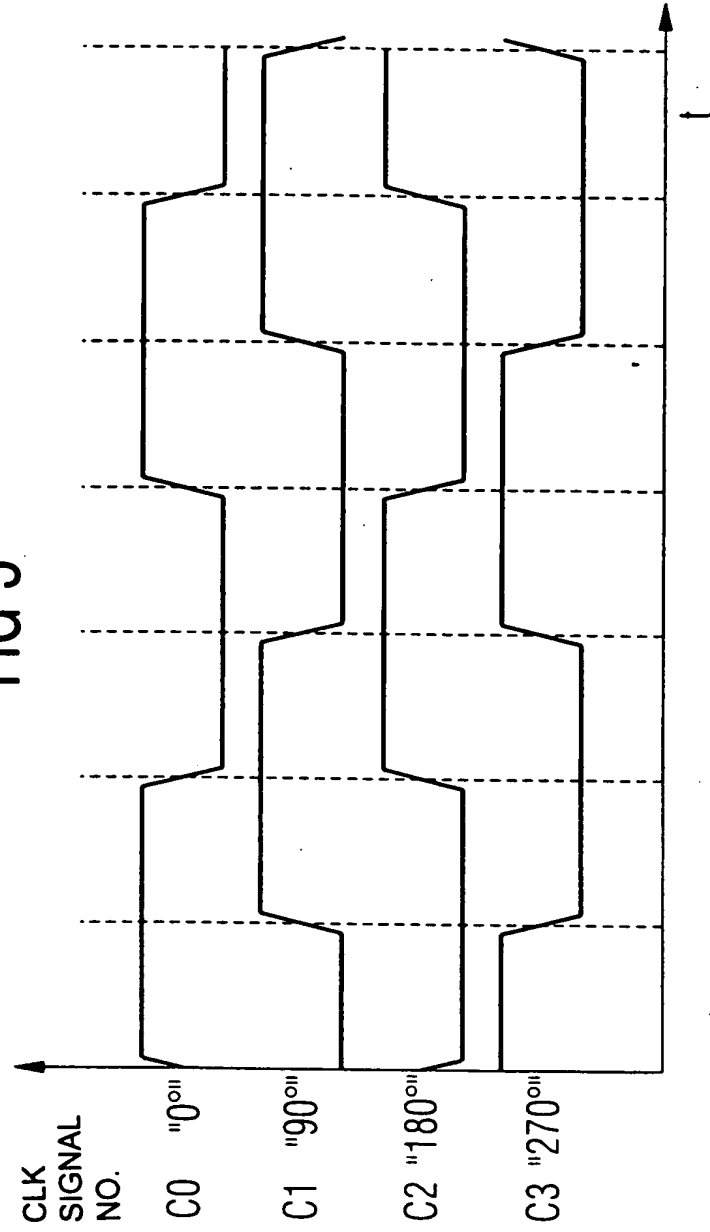


FIG 6

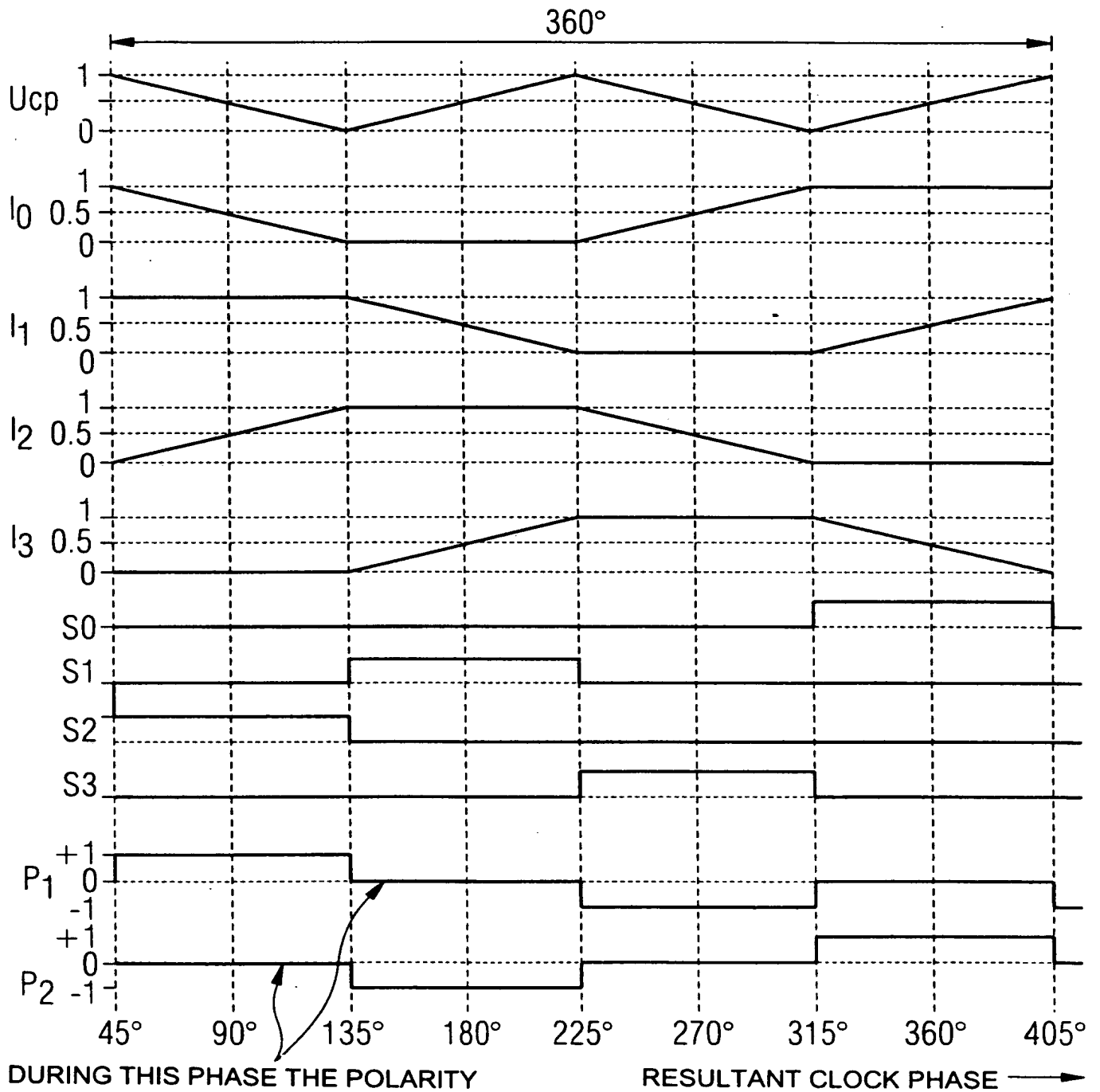


FIG 7

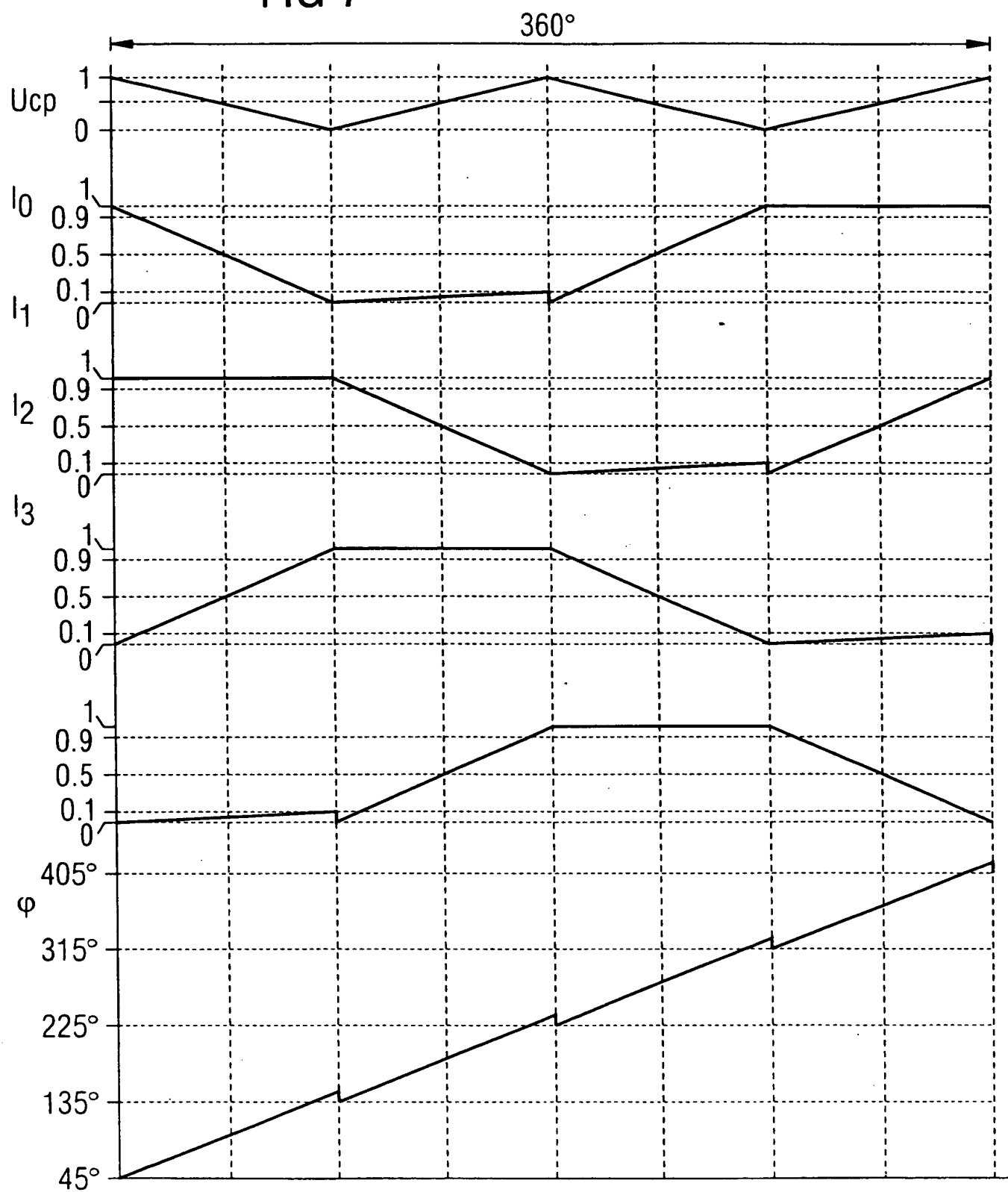


FIG 8

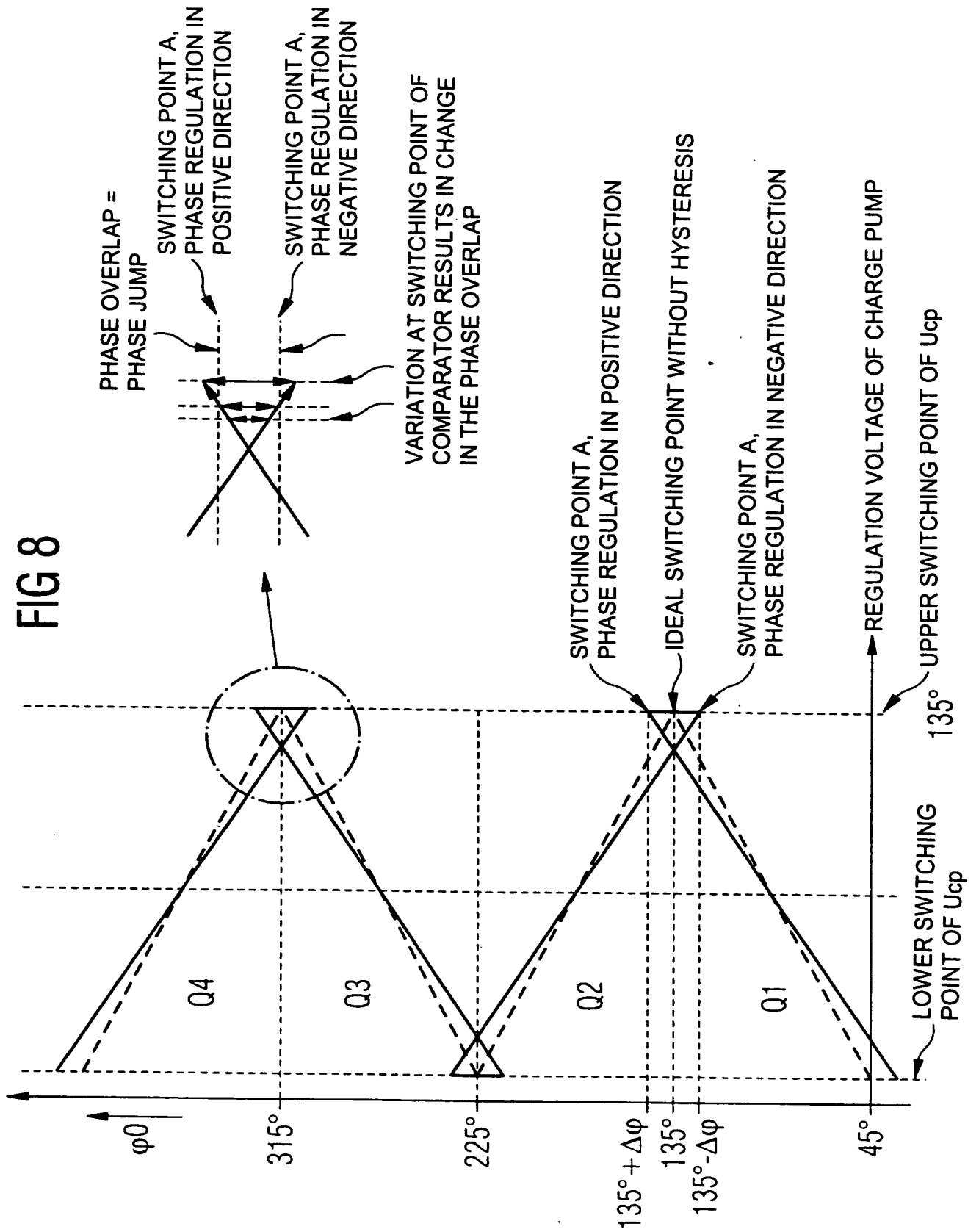


FIG 9

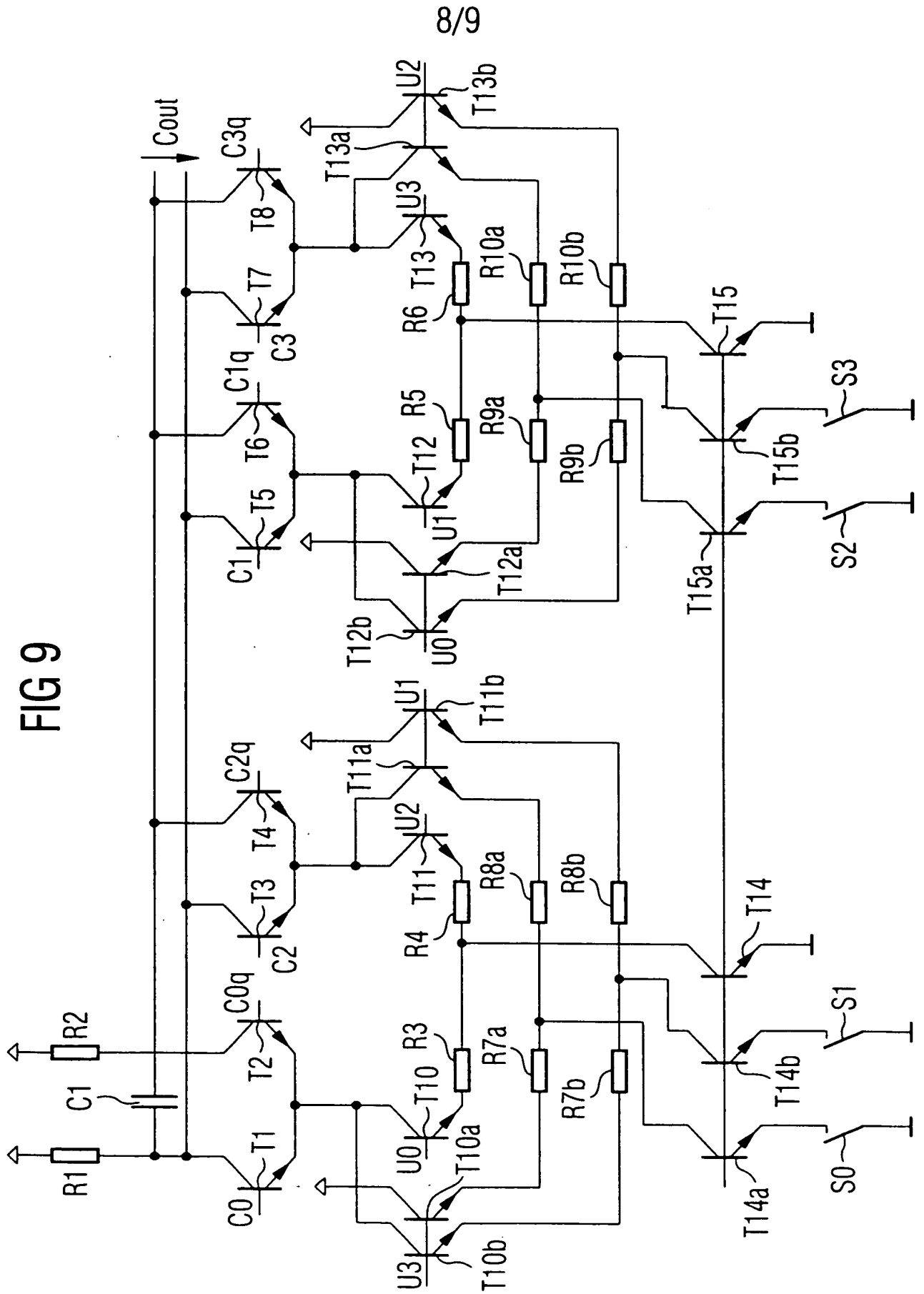


FIG 10

	45°-135°	135°-225°	225°-315°	315°-405°
S0	0	0	0	1
S1	0	1	0	0
S2	1	0	0	0
S3	0	0	1	0
U0 (POSITIVE OUTPUT FROM SDiff1)	POSITIVE	low	NEGATIVE	high
U1 (POSITIVE OUTPUT FROM SDiff2)	high	NEGATIVE	low	POSITIVE
U2 (NEGATIVE OUTPUT FROM SDiff1)	NEGATIVE	high	POSITIVE	low
U3 (NEGATIVE OUTPUT FROM SDiff2)	low	POSITIVE	high	NEGATIVE
G1 (GAIN OF SDiff1)	+1	0	-1	0
G2 (GAIN OF SDiff2)	0	-1	0	+1